Insert Sort on SCMPDS¹

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Summary. The goal of this article is to examine the effectiveness of "forloop" and "while-loop" statements on SCMPDS by insert sort. In this article, first of all, we present an approach to compute the execution result of "for-loop" program by "loop-invariant", based on Hoare's axioms for program verification. Secondly, we extend the fundamental properties of the finite sequence and complex instructions of SCMPDS. Finally, we prove the correctness of the insert sort program described in the article.

MML Identifier: SCPISORT.

The terminology and notation used in this paper have been introduced in the following articles: [16], [19], [1], [3], [4], [20], [2], [13], [15], [9], [5], [8], [6], [7], [12], [10], [11], [17], [21], [18], and [14].

1. Preliminaries

In this paper n, p_0 are natural numbers.

Let f be a finite sequence of elements of \mathbb{Z} , let s be a state of SCMPDS, and let m be a natural number. We say that f is FinSequence on s, m if and only if:

(Def. 1) For every natural number i such that $1 \leq i$ and $i \leq \text{len } f$ holds f(i) = s(intpos m + i).

We now state four propositions:

(1) Let f be a finite sequence of elements of \mathbb{Z} and m, n be natural numbers. If $m \ge n$, then f is non decreasing on m, n.

C 2001 University of Białystok ISSN 1426-2630

¹This research is partially supported by the National Natural Science Foundation of China Grant No. 69873033.

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- (2) Let s be a state of SCMPDS and n, m be natural numbers. Then there exists a finite sequence f of elements of \mathbb{Z} such that len f = n and for every natural number i such that $1 \leq i$ and $i \leq \text{len } f$ holds f(i) = s(intpos m+i).
- (3) Let s be a state of SCMPDS and n, m be natural numbers. Then there exists a finite sequence f of elements of \mathbb{Z} such that len f = n and f is FinSequence on s, m.
- (4) Let f, g be finite sequences of elements of \mathbb{Z} and m, n be natural numbers. Suppose that $1 \leq n$ and $n \leq \text{len } f$ and $1 \leq m$ and $m \leq \text{len } f$ and len f = len g and f(m) = g(n) and f(n) = g(m) and for every natural number k such that $k \neq m$ and $k \neq n$ and $1 \leq k$ and $k \leq \text{len } f$ holds f(k) = g(k). Then f and g are fiberwise equipotent.

The following propositions are true:

- (5) For all states s_1 , s_2 of SCMPDS such that for every Int position a holds $s_1(a) = s_2(a)$ holds Destate $s_1 = D$ state s_2 .
- (6) Let s be a state of SCMPDS, I be a No-StopCode Program-block, and j be a parahalting shiftable instruction of SCMPDS. Suppose I is closed on s and halting on s. Then I; j is closed on s and I; j is halting on s.
- (7) Let s be a state of SCMPDS, I be a No-StopCode Program-block, J be a shiftable parahalting Program-block, and a be an Int position. If I is closed on s and halting on s, then (IExec(I; J, s))(a) = (IExec(J, IExec(I, s)))(a).
- (8) Let s be a state of SCMPDS, I be a No-StopCode parahalting Programblock, J be a shiftable Program-block, and a be an Int position. If J is closed on IExec(I, s) and halting on IExec(I, s), then (IExec(I; J, s))(a) =(IExec(J, IExec(I, s)))(a).
- (9) Let s be a state of SCMPDS, I be a Program-block, and J be a shiftable parahalting Program-block. Suppose I is closed on s and halting on s. Then I; J is closed on s and I; J is halting on s.
- (10) Let s be a state of SCMPDS, I be a parahalting Program-block, and J be a shiftable Program-block. Suppose J is closed on IExec(I, s) and halting on IExec(I, s). Then I; J is closed on s and I; J is halting on s.
- (11) Let s be a state of SCMPDS, I be a Program-block, and j be a parahalting shiftable instruction of SCMPDS. Suppose I is closed on s and halting on s. Then I; j is closed on s and I; j is halting on s.

2. Computing the Execution Result of For-Loop Program by Loop-Invariant

In this article we present several logical schemes. The scheme ForDownHalt deals with a state \mathcal{A} of SCMPDS, a No-StopCode shiftable Program-block \mathcal{B} ,

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an Int position C, an integer D, a natural number \mathcal{E} , and a unary predicate \mathcal{P} , and states that:

 $\mathcal{P}[\mathcal{A}]$ or not $\mathcal{P}[\mathcal{A}]$ but for-down $(\mathcal{C}, \mathcal{D}, \mathcal{E}, \mathcal{B})$ is closed on \mathcal{A} but for-down $(\mathcal{C}, \mathcal{D}, \mathcal{E}, \mathcal{B})$ is halting on \mathcal{A}

provided the following requirements are met:

- $\mathcal{E} > 0$,
- $\mathcal{P}[Dstate \mathcal{A}]$, and
- Let t be a state of SCMPDS. Suppose $\mathcal{P}[\text{Dstate } t]$ and $t(\mathcal{C}) = \mathcal{A}(\mathcal{C})$ and $t(\text{DataLoc}(\mathcal{A}(\mathcal{C}), \mathcal{D})) > 0$. Then $(\text{IExec}(\mathcal{B}; \text{AddTo}(\mathcal{C}, \mathcal{D}, -\mathcal{E}), t))(\mathcal{C}) = t(\mathcal{C})$ and $(\text{IExec}(\mathcal{B}; \text{AddTo}(\mathcal{C}, \mathcal{D}, -\mathcal{E}), t))(\text{DataLoc}(\mathcal{A}(\mathcal{C}), \mathcal{D})) = t(\text{DataLoc}(\mathcal{A}(\mathcal{C}), \mathcal{D})) - \mathcal{E}$ and \mathcal{B} is closed on t and \mathcal{B} is halting on t and $\mathcal{P}[\text{Dstate IExec}(\mathcal{B}; \text{AddTo}(\mathcal{C}, \mathcal{D}, -\mathcal{E}), t)].$

The scheme *ForDownExec* deals with a state \mathcal{A} of SCMPDS, a No-StopCode shiftable Program-block \mathcal{B} , an Int position \mathcal{C} , an integer \mathcal{D} , a natural number \mathcal{E} , and a unary predicate \mathcal{P} , and states that:

 $\mathcal{P}[\mathcal{A}] \quad \text{or} \quad \text{not} \quad \mathcal{P}[\mathcal{A}] \quad \text{but} \quad \text{IExec}(\text{for-down}(\mathcal{C}, \mathcal{D}, \mathcal{E}, \mathcal{B}), \mathcal{A}) = \mathcal{P}[\mathcal{A}] \quad \text{or} \quad \text{int} \quad \mathcal{P}[\mathcal{A}] \quad \text{or} \quad \text{or} \quad \mathcal{P}[\mathcal{A}] \quad \mathcal{P}[\mathcal{A}] \quad \text{or} \quad \mathcal{P}[\mathcal{A}] \quad \text{or} \quad \mathcal{P}[\mathcal{A}] \quad \text{or} \quad \mathcal{P}[\mathcal{A}] \quad \text{or} \quad \mathcal{P}[\mathcal{A}] \quad \mathcal{P}[\mathcal{A$

 $\operatorname{IExec}(\operatorname{for-down}(\mathcal{C},\mathcal{D},\mathcal{E},\mathcal{B}),\operatorname{IExec}(\mathcal{B}; \operatorname{AddTo}(\mathcal{C},\mathcal{D},-\mathcal{E}),\mathcal{A}))$

provided the parameters meet the following conditions:

- $\mathcal{E} > 0$,
- $\mathcal{A}(\text{DataLoc}(\mathcal{A}(\mathcal{C}), \mathcal{D})) > 0,$
- $\mathcal{P}[Dstate \mathcal{A}]$, and
- Let t be a state of SCMPDS. Suppose $\mathcal{P}[\text{Dstate } t]$ and $t(\mathcal{C}) = \mathcal{A}(\mathcal{C})$ and $t(\text{DataLoc}(\mathcal{A}(\mathcal{C}), \mathcal{D})) > 0$. Then $(\text{IExec}(\mathcal{B}; \text{AddTo}(\mathcal{C}, \mathcal{D}, -\mathcal{E}), t))(\mathcal{C}) = t(\mathcal{C})$ and $(\text{IExec}(\mathcal{B}; \text{AddTo}(\mathcal{C}, \mathcal{D}, -\mathcal{E}), t))(\text{DataLoc}(\mathcal{A}(\mathcal{C}), \mathcal{D})) = t(\text{DataLoc}(\mathcal{A}(\mathcal{C}), \mathcal{D})) - \mathcal{E}$ and \mathcal{B} is closed on t and \mathcal{B} is halting on t and $\mathcal{P}[\text{Dstate IExec}(\mathcal{B}; \text{AddTo}(\mathcal{C}, \mathcal{D}, -\mathcal{E}), t)].$

The scheme *ForDownEnd* deals with a state \mathcal{A} of SCMPDS, a No-StopCode shiftable Program-block \mathcal{B} , an Int position \mathcal{C} , an integer \mathcal{D} , a natural number \mathcal{E} , and a unary predicate \mathcal{P} , and states that:

 $\mathcal{P}[\mathcal{A}] \text{ or not } \mathcal{P}[\mathcal{A}] \text{ but } (\text{IExec}(\text{for-down}(\mathcal{C}, \mathcal{D}, \mathcal{E}, \mathcal{B}), \mathcal{A}))(\text{DataLoc}(\mathcal{A}(\mathcal{C}), \mathcal{D})) \leq 0 \text{ but } \mathcal{P}[\text{Dstate IExec}(\text{for-down}(\mathcal{C}, \mathcal{D}, \mathcal{E}, \mathcal{B}), \mathcal{A})]$

provided the parameters have the following properties:

- $\mathcal{E} > 0$,
- $\mathcal{P}[Dstate \mathcal{A}]$, and
- Let t be a state of SCMPDS. Suppose $\mathcal{P}[\text{Dstate } t]$ and $t(\mathcal{C}) = \mathcal{A}(\mathcal{C})$ and $t(\text{DataLoc}(\mathcal{A}(\mathcal{C}), \mathcal{D})) > 0$. Then $(\text{IExec}(\mathcal{B}; \text{AddTo}(\mathcal{C}, \mathcal{D}, -\mathcal{E}), t))(\mathcal{C}) = t(\mathcal{C})$ and $(\text{IExec}(\mathcal{B}; \text{AddTo}(\mathcal{C}, \mathcal{D}, -\mathcal{E}), t))(\text{DataLoc}(\mathcal{A}(\mathcal{C}), \mathcal{D})) = t(\text{DataLoc}(\mathcal{A}(\mathcal{C}), \mathcal{D})) - \mathcal{E}$ and \mathcal{B} is closed on t and \mathcal{B} is halting on t and $\mathcal{P}[\text{Dstate IExec}(\mathcal{B}; \text{AddTo}(\mathcal{C}, \mathcal{D}, -\mathcal{E}), t)].$

We now state three propositions:

(12) Let s be a state of SCMPDS, I be a No-StopCode shiftable Programblock, a, x, y be Int positions, i, c be integers, and n be a natural number. Suppose that

- (i) n > 0,
- (ii) $s(x) \ge s(y) + c$, and
- (iii) for every state t of SCMPDS such that $t(x) \ge t(y) + c$ and t(a) = s(a)and t(DataLoc(s(a), i)) > 0 holds (IExec(I; AddTo(a, i, -n), t))(a) = t(a)and (IExec(I; AddTo(a, i, -n), t))(DataLoc(s(a), i)) = t(DataLoc(s(a), i))-n and I is closed on t and halting on t and $(\text{IExec}(I; \text{AddTo}(a, i, -n), t))(x) \ge (\text{IExec}(I; \text{AddTo}(a, i, -n), t))(y) + c.$ Then for-down(a, i, n, I) is closed on s and for-down(a, i, n, I) is halting
 - on s.
- (13) Let s be a state of SCMPDS, I be a No-StopCode shiftable Programblock, a, x, y be Int positions, i, c be integers, and n be a natural number. Suppose that
 - (i) n > 0,
 - (ii) $s(x) \ge s(y) + c$,
- (iii) s(DataLoc(s(a), i)) > 0, and
- (iv) for every state t of SCMPDS such that $t(x) \ge t(y) + c$ and t(a) = s(a)and t(DataLoc(s(a), i)) > 0 holds (IExec(I; AddTo(a, i, -n), t))(a) = t(a)and (IExec(I; AddTo(a, i, -n), t))(DataLoc(s(a), i)) = t(DataLoc(s(a), i))-n and I is closed on t and halting on t and $(\text{IExec}(I; \text{AddTo}(a, i, -n), t))(x) \ge (\text{IExec}(I; \text{AddTo}(a, i, -n), t))(y) + c.$ Then IExec(for-down(a, i, n, I), s) = IExec(for-down(a, i, n, I), I)(IExec(I; AddTo(a, i, -n), s)).
- (14) Let s be a state of SCMPDS, I be a No-StopCode shiftable Programblock, a be an Int position, i be an integer, and n be a natural number. Suppose that
 - (i) s(DataLoc(s(a), i)) > 0,
 - (ii) n > 0,
- (iii) $\operatorname{card} I > 0,$
- (iv) $a \neq \text{DataLoc}(s(a), i)$, and
- (v) for every state t of SCMPDS such that t(a) = s(a)holds (IExec(I,t))(a) = t(a) and (IExec(I,t))(DataLoc(s(a),i)) =t(DataLoc(s(a),i)) and I is closed on t and halting on t. Then for-down(a, i, n, I) is closed on s and for-down(a, i, n, I) is halting on s.

3. A Program for Insert Sort

Let n, p_0 be natural numbers. The functor insert-sort (n, p_0) yielding a Program-block is defined by the condition (Def. 2).

4. The Property of Insert Sort and Its Correctness

We now state two propositions:

- (15) card insert-sort $(n, p_0) = 23$.
- (16) If $p_0 \ge 7$, then insert-sort (n, p_0) is parahalting.

One can prove the following propositions:

- (17) Let s be a state of SCMPDS, f, g be finite sequences of elements of \mathbb{Z} , and k_0 , k be natural numbers. Suppose that $s(a_4) \ge 7 + s(a_6)$ and s(GBP) = 0 and $k = s(a_6)$ and $k_0 = s(a_4) s(a_6) 1$ and f is FinSequence on s, k_0 and g is FinSequence on IExec (I_2, s) , k_0 and len f = len g and len f > k and f is non decreasing on 1, k. Then
 - (i) f and g are fiberwise equipotent,
 - (ii) g is non decreasing on 1, k + 1,
- (iii) for every natural number i such that i > k + 1 and $i \leq \text{len } f$ holds f(i) = g(i), and
- (iv) for every natural number *i* such that $1 \le i$ and $i \le k+1$ there exists a natural number *j* such that $1 \le j$ and $j \le k+1$ and g(i) = f(j), where a_4 = intpos 4, a_6 = intpos 6, $I_2 = W_1$, W_1 = while > $0(\text{GBP}, 6, B_1)$, $B_1 = k_1$; k_2 ; I_1 , $k_1 = (\text{GBP}, 5) := (\text{intpos } 4, -1)$, $k_2 = \text{SubFrom}(\text{GBP}, 5, \text{intpos } 4, 0)$, $I_1 = \text{if GBP} > 5$ then T_1 else F_1 , $T_1 = k_3$; k_4 ; k_5 ; k_6 ; k_7 , $k_3 = (\text{GBP}, 5) := (\text{intpos } 4, -1)$, $k_4 = (\text{intpos } 4, -1) := (\text{intpos } 4, 0)$, $k_5 = (\text{intpos } 4, 0) := (\text{GBP}, 5)$, $k_6 = \text{AddTo}(\text{GBP}, 4, -1)$, $k_7 = \text{AddTo}(\text{GBP}, 6, -1)$, and $F_1 = \text{Load}((\text{GBP})_6 := 0)$.
- (18) Let s be a state of SCMPDS, f, g be finite sequences of elements of \mathbb{Z} , and p_0, n be natural numbers. Suppose $p_0 \ge 6$ and len f = n and len g = n and f is FinSequence on s, p_0 and g is FinSequence on IExec(insert-sort($n, p_0 + 1$), s), p_0 . Then f and g are fiberwise equipotent and g is non decreasing on 1, n.

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Received June 14, 2000